

Reactis V2024.2

Released November 21, 2024



Improved Static Analysis and Test Coverage

Improvements to Tester engine

- Identify more unreachable targets
- Exercise more coverage targets

Especially for

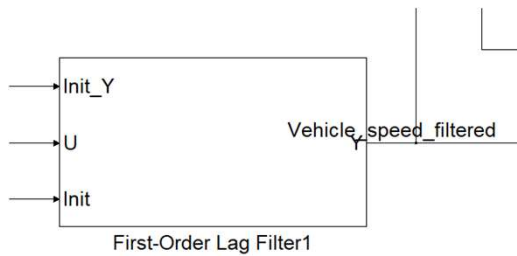
- MC/DC and MCC targets
- Conditionally executed subsystems
- Blocks with rounding settings

Performance Improvements for Large Models

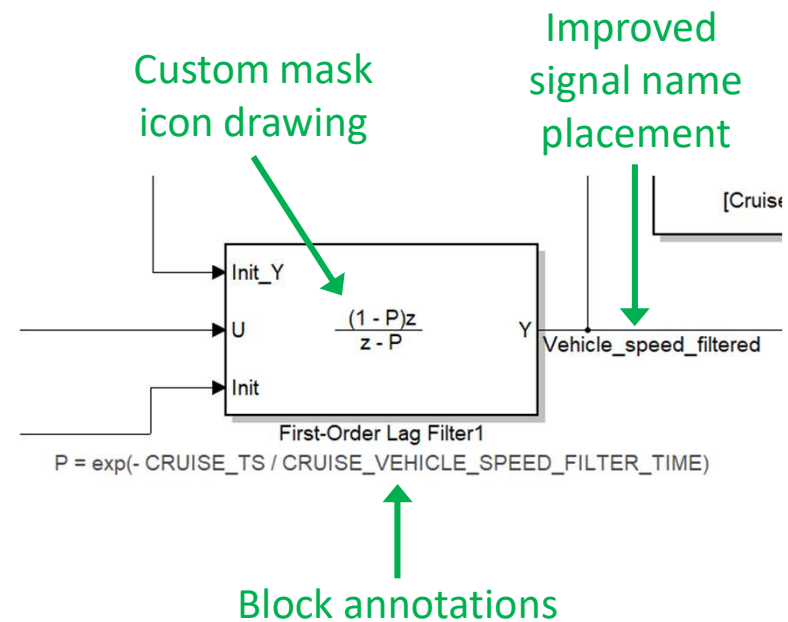
- Some models import 50% faster and use 65% less memory
- MATLAB invocation setting no longer causes slow import for models with large workspaces

Improvements to Model Rendering

V2024

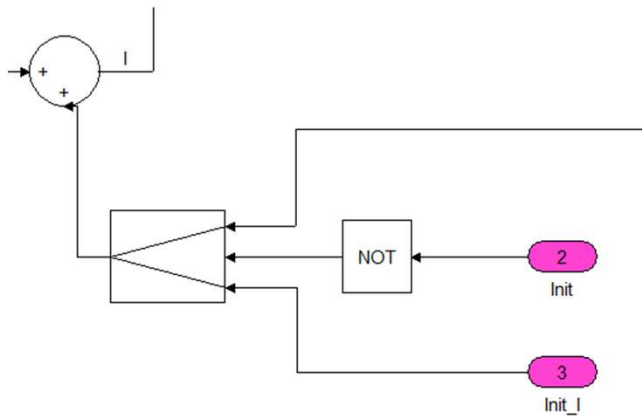


V2024.2

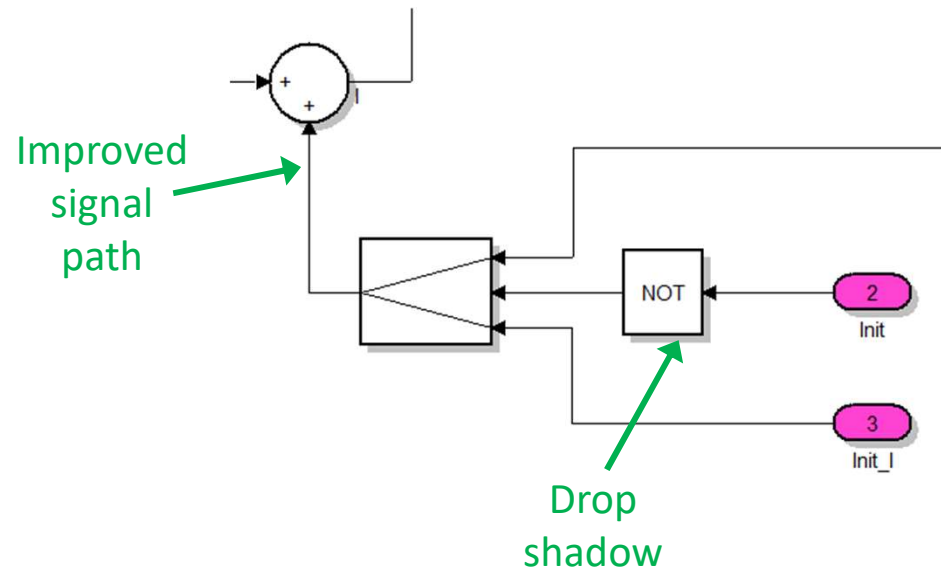


Improvements to Model Rendering

V2024

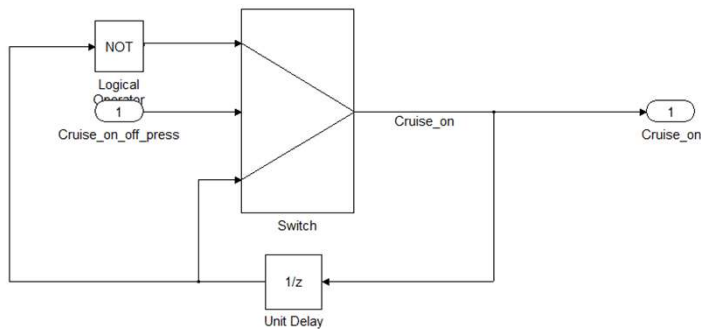


V2024.2



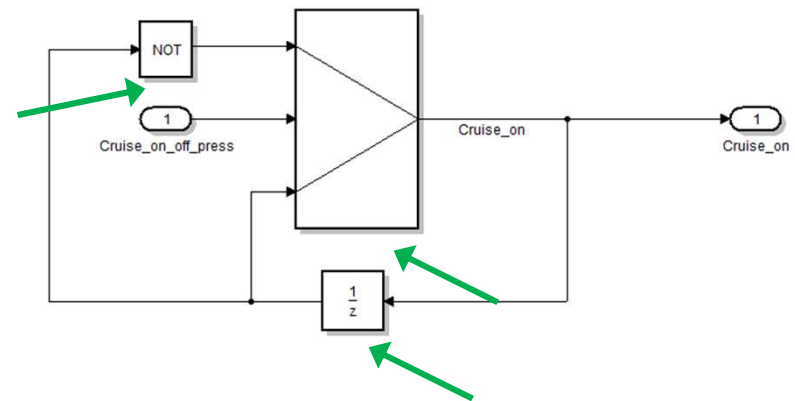
Improvements to Model Rendering

V2024



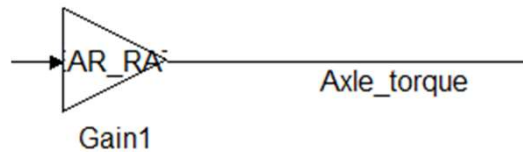
V2024.2

If Format > Show Block Name set to Auto, do not display default block name



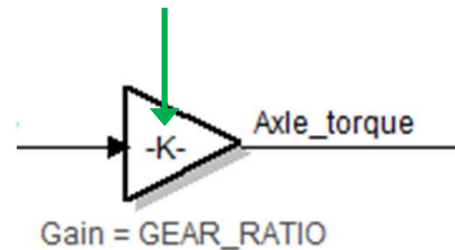
Improvements to Model Rendering

V2024



V2024.2

Don't display gain value
if it is too long



Rendering Improvements: Mask Icon Drawing

```
disp('Hello')
```



disp1

```
dpoly([5 2 1], [1 2], 'x-');
```

$$\frac{5+2x^{-1}+x^{-2}}{1+2x^{-1}}$$

dpoly3

```
droots([-10,-5],[2,-6],2,'z');
```

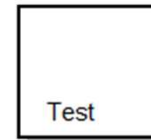
$$\frac{2(z+10)(z+5)}{(z-2)(z+6)}$$

droot12

```
image('reactis_logo.jpg');
```

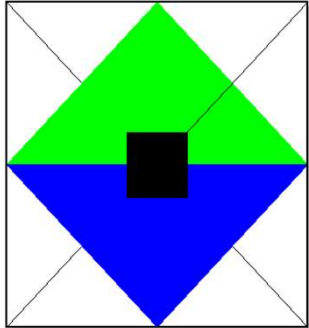


```
text(0.2, 0.2, 'Test');
```

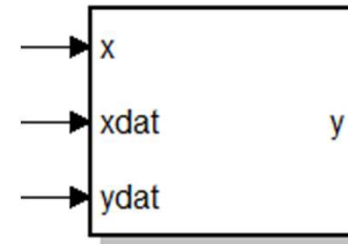


Rendering Improvements: Mask Icon Drawing

```
plot([1 3],[1,-1]);  
patch([1,2,3],[0,1,0],[0 1 0]);  
plot([1,3],[-1,1]);  
patch([1,2,3],[0,-1,0],[0 0 1]);  
patch([1.8,2.2,2.2,1.8],[0.2,0.2,-0.2,-0.2]);
```

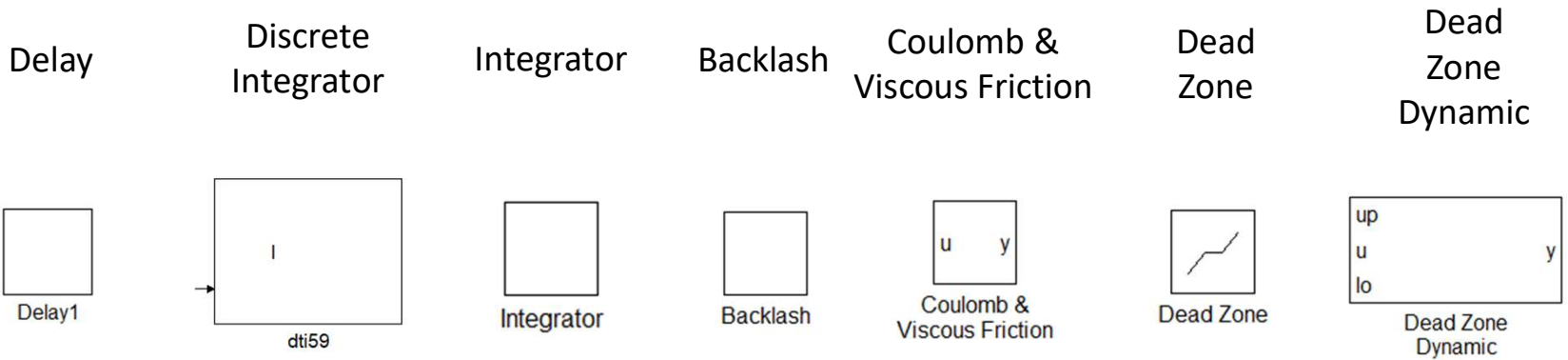


```
port_label('Input',1,'x');  
port_label('Input',2,'xdat');  
port_label('Input',3,'ydat');  
port_label('Output',1,'y');
```

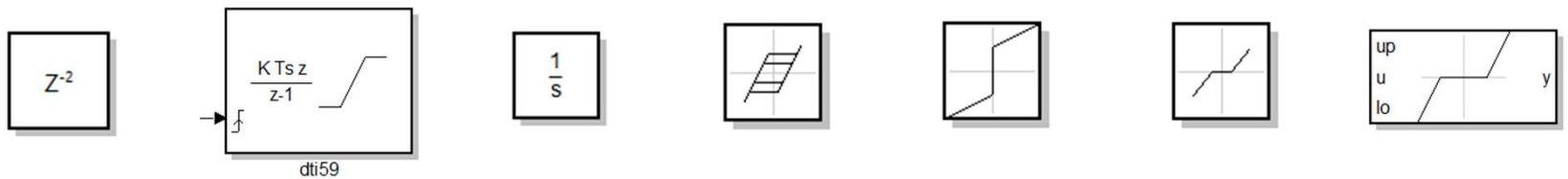


Rendering Improvements: Native Blocks

V2024



V2024.2



Rendering Improvements: Native Blocks

V2024

Quantizer



Quantizer

Relay



Relay

Saturation
Dynamic



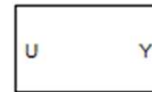
Saturation
Dynamic

Wrap
to Zero



Wrap To Zero

Difference



Difference

Discrete
Derivative



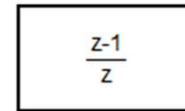
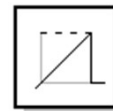
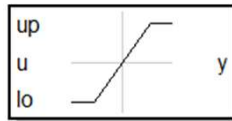
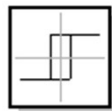
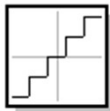
Discrete Derivative

Discrete
State
Space

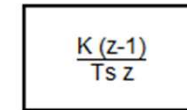


Discrete State-Space

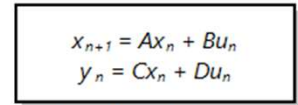
V2024.2



Difference



Discrete Derivative



Discrete State-Space

Rendering Improvements: Native Blocks

V2024

Enabled Delay



Enabled Delay

Memory



Memory

Propagation Delay



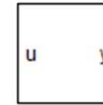
Propagation Delay

Zero-Order Hold



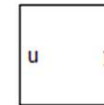
Zero-Order Hold

Bit Clear



Bit Clear

Bit Set



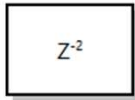
Bit Set

Combinatorial Logic

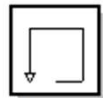


Combinatorial Logic

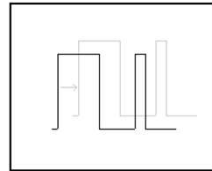
V2024.2



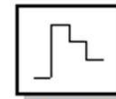
Enabled Delay



Memory



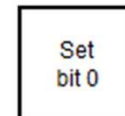
Propagation Delay



Zero-Order Hold



Clear bit 0



Set bit 0



Rendering Improvements: Native Blocks

V2024

Detect Decrease



Detect Decrease

Detect Increase



Detect Increase

Interval Test



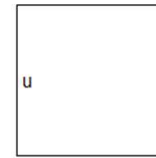
Interval Test

Interval Test Dynamic



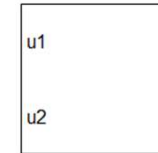
Interval Test Dynamic

1-D Lookup Table



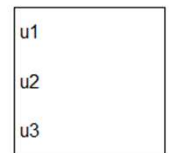
1-D Lookup Table

2-D Lookup Table



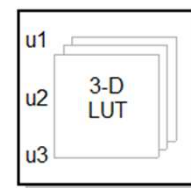
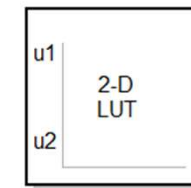
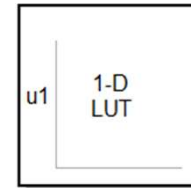
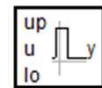
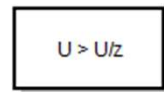
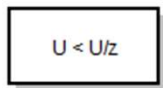
2-D Lookup Table

3-D Lookup Table



n-D Lookup Table

V2024.2



Rendering Improvements: Native Blocks

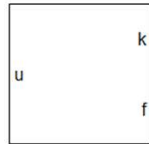
V2024

Direct
Lookup
Table



Direct Lookup
Table (n-D)

Prelookup



Prelookup

Complex to
Magnitude-
Angle



Complex to
Magnitude-Angle

Complex to
Real-Imag



Complex to
Real-Imag

Sqrt



Sqrt

Create
Diagonal
Matrix



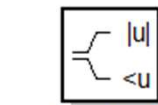
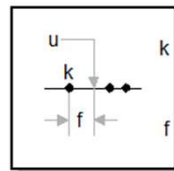
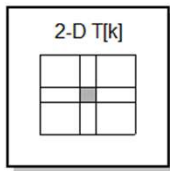
Create Diagonal
Matrix

Extract
Diagonal

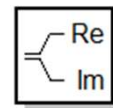


Extract Diagonal

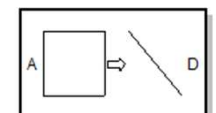
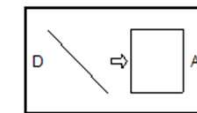
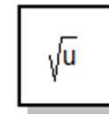
V2024.2



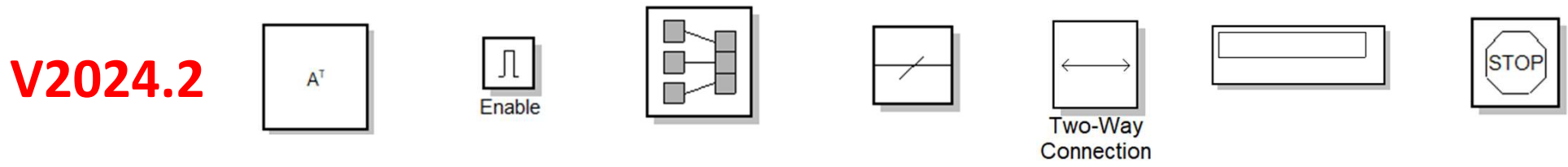
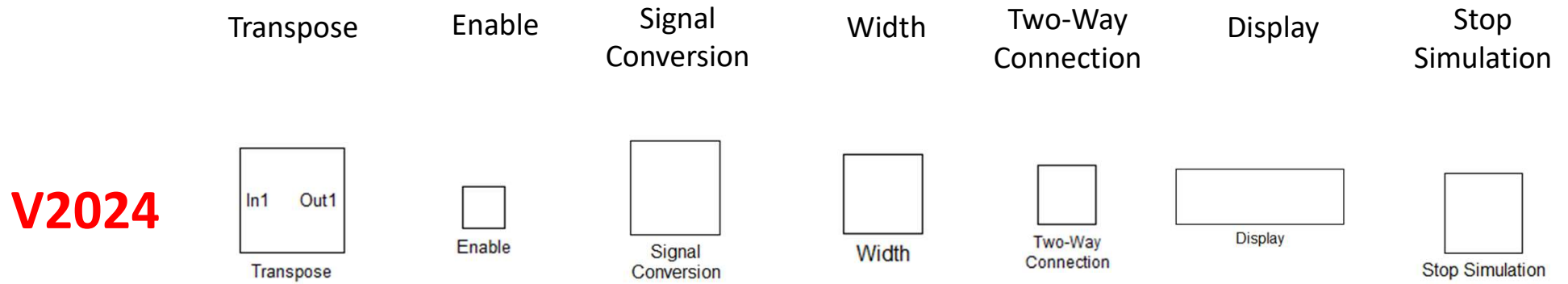
Complex to
Magnitude-Angle



Complex to
Real-Imag



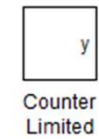
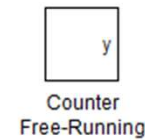
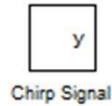
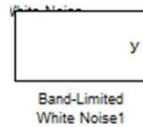
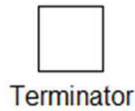
Rendering Improvements: Native Blocks



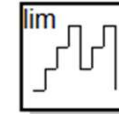
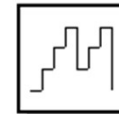
Rendering Improvements: Native Blocks

Terminator Band-Limited White Noise Chirp Signal Clock Counter Free-Running Counter Limited Ground

V2024



V2024.2



Rendering Improvements: Native Blocks

Pulse Generator

Ramp

Repeating Sequence

Signal Generator

Step

Waveform Generator

V2024



Pulse Generator



Ramp



Repeating Sequence



Signal Generator

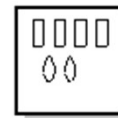
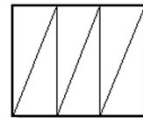


Step

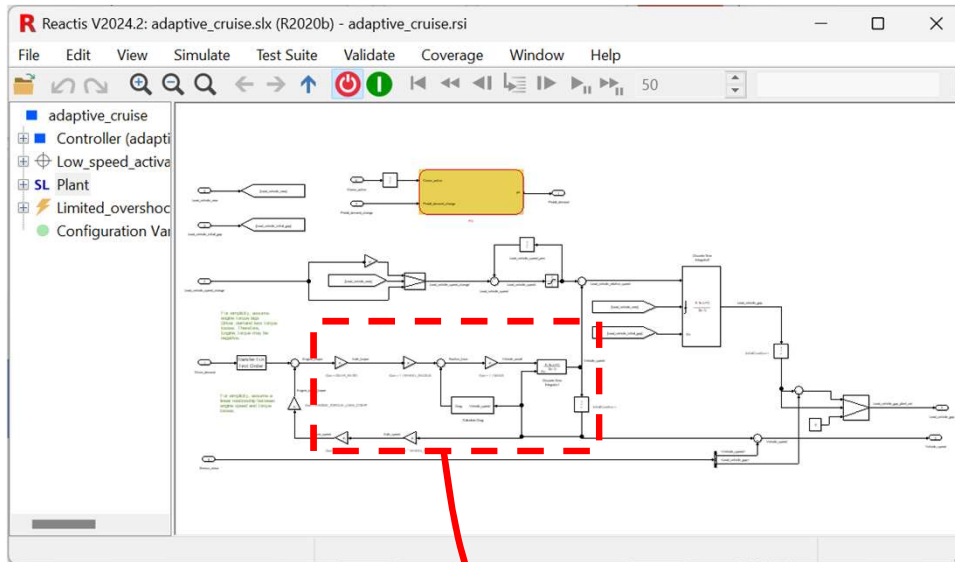


Waveform Generator

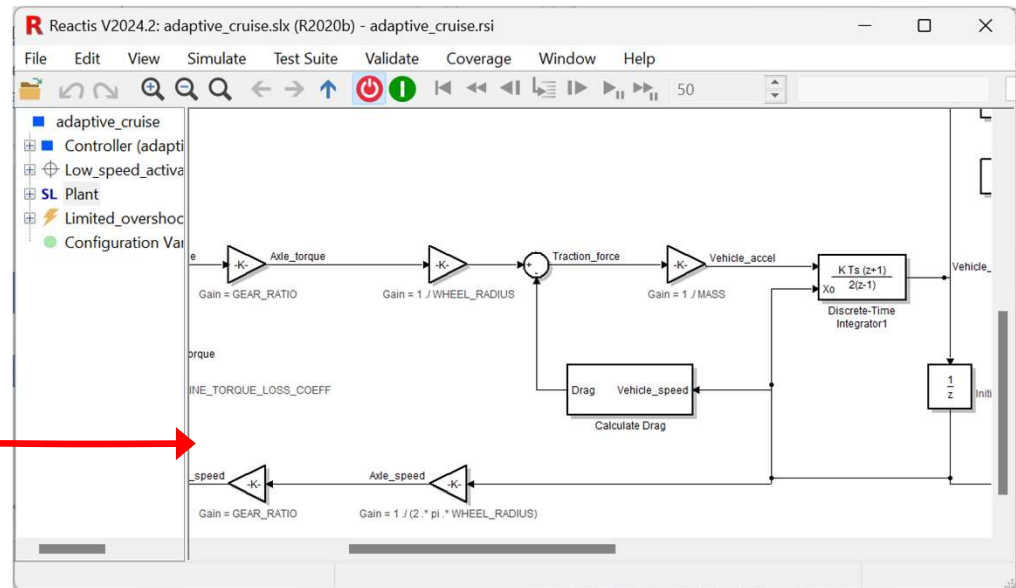
V2024.2



Zoom to Region of Interest



Control-click drag
to select region of interest



New Embedded MATLAB (EML) Support

Newly supported functions:

- num2str, str2num, strlen, strcmp, char, exist, isfloat, isinteger, islogical

Newly Supported Simulink Features

- MATLAB R2024b.
- Subsystem Reference blocks
- Outport parameter **Ensure Outport is Virtual**